

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Leonard Forbes	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	303.620US2
Title:	PROGRAMMABLE FUSE AND ANTIFUSE AND METHOD THEREFOR		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. application, Serial No. 09/782543, filed on February 13, 2001, which is relied upon for an earlier filing date under 35 U.S.C. §120.

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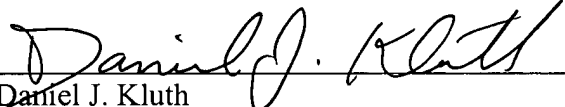
The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

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Date Nov. 20, 2003 By 
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	Unknown
Filing Date	Even Date Herewith
First Named Inventor	Forbes, Leonard
Group Art Unit	Unknown
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 303.620US2

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	US-5,324,681	06/28/1994	Lowrey, T. A., et al.	437	52	02/22/1993
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	US-5,493,141	02/20/1996	Ricco, B., et al.	257	321	04/21/1994
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	US-5,768,192	06/16/1998	Eitan, Boaz	365	185.24	07/23/1996
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		EITAN, BOAZ, "NROM: A Novel Localized Trapping, 2-Bit Nonvolatile Memory Cell", <u>IEEE Electron Device Letters</u> , 21(11), (November 2000),543-545	
		FISCHETTI, M. V., et al., "The effect of gate metal and SiO2 thickness on the generation of donor states at the Si-SiO2 interface", <u>Journal of Applied Physics</u> , vol. 57, no. 2, (January 1985),418-424	
		LUSKY, ELI, et al., "Spatial characterization of Channel hot electron injection Utilizing subthreshold slope of NROM memory device", pp. 1-2	
		NING, T. H., "Capture cross section and trap concentration of holes in silicon dioxide", <u>Journal of Applied Physics</u> , vol. 47, no. 3, (March 1976),1079-1081	
		NING, T. H., et al., "Completely electrically reprogrammable nonvolatile memory device using conventional p-channel MOSFET", <u>IBM Technical Disclosure Bulletin</u> , vol. 20, no. 5, (October 1977),2016	
		NING, T. H., et al., "Erasable nonvolatile memory device using hole trapping in SiO2", <u>IBM Technical Disclosure Bulletin</u> , vol. 18, no. 8, (January 1976),2740-2742	
		ROIZIN, YAKOV, et al., "'Dummy' GOX for Optimization of microFLASH Technology", 2 pages	
		ROIZIN, YAKOV, et al., "Activation Energy of Traps in the ONO Stack of microFLASH Memory Cells", 2 pages	
		SAMANTA, PIYAS, et al., "Coupled charge trapping dynamics in thin SiO2 gate oxide under Fowler-Nordheim stress at low electron fluence", <u>Journal of Applied Physics</u> , vol. 83, no. 5, (March 1998),2662-2669	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached